

PATENT

**RESPONSE UNDER 37 C.F.R. 1.116 - EXPEDITED
PROCEDURE - EXAMINING GROUP 2814**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.:	10/762,789	Confirmation No.	6124
Applicant(s):	Douglas G. Anderson		
Filed:	January 22, 2004		
Art Unit:	2814		
Examiner:	Abdul Kalam		
Title:	WAFER, INTERMEDIATE WAFER ASSEMBLY AND ASSOCIATED METHOD FOR FABRICATING A SILICON ON INSULATOR WAFER HAVING AN IMPROVED EDGE PROFILE		

Docket No.: 039035/267930
Customer No.: 00826

July 20, 2007

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO FINAL OFFICIAL ACTION

Sir:

In response to the Final Official Action dated March 20, 2007, please reconsider the above-identified patent application in view of the following:

Remarks begin on page 2 of this paper.